

METHOD OF TESTING PRINTED CIRCUIT BOARD OPENING SPACINGS

ABSTRACT

A method for testing opening patterns in an active area of a PCB wherein two arrays of test patterns of apertured pads are analyzed following drilling therethrough in accordance with a specified manner. Specifically, the outer patterns are first tested and if failure results in one or more of said patterns, an inner array of patterns closer to the active area are then tested and, significantly, only the respective test pattern nearest the associated array of openings is used to determine whether said array of openings meets the designated spacing criteria.